FORM PTO-1449								ty. Docket -0933/L13	Appl. No.: 10/64803		
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT								First Named Inventor: Alexander E. Andreev et al.			
								REWITH			
Examiner Initial		Document No.	Date		Name			Class	Sub Class	Filing Date If Appropriate	
00	AA	10/021,414			Eneboe et al.					10/30/2001	
Ollo	AB	10/299,270			Andreev et al.					11/19/2002	
(00	AC	4,656,592	4/1987		Spaanenburg et al.			364	490		
All	AD	5,752,070	5/1998		Martin et al.			395	800.33		
000	AE	5,898,677	4/1999		Deeley et al.			370	276		
The	AF	6,152,613	11/2000		Martin et al.			395	800.33		
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		Document No.		Da	Date Country			Clas	Sub S Class	Translation Yes No	
	AL								1	1	
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	AN										
		OTHER ART	Includi	ng A	uthor,	Title, Date, 1	Pert	inent Pag	es, Etc.)		
20	AO	"On-chip Networks weighed as wiring alternative," by R. Wilson, Integrated System Design, June 25, 2001, URL:http://www.eet.com/OEG20010625S0099									
la	AP	"Configurable Computing," by J. Villasenor et al.; Scientific American, June 1997, URL:http://sciam.com/0697issue/0697villasenor.html									
lie	AQ	"Models of Computation, Exploring the Power of Computing" by John E. Savage, pp. 306-341, 1998									
	ER: (1717.1111				DATE CO			6/8/0		

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.